

U.S. Department of Commerce, Patent and Trademark Office	Application No.: 09/802,551
	Filing date: March 9, 2001
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Inventors: Clive M. Philbrick et al.
	Group Art Unit: 2182
INTELLIGENT NETWORK STORAGE INTERFACE	Examiner name: unknown
	Attorney Docket No. ALA-012

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	A	4,366,538	12/28/82	Johnson et al.	364	200	
	B	4,991,133	05/13/86	Davis et al.	364	900	
	C	5,056,058	10/08/91	Hirata et al.	709	230	
	D	5,097,442	03/17/92	Ward et al.	365	78	
	E	5,163,131	11/10/92	Row et al.	395	200	
	F	5,212,778	05/18/93	Dally et al.	395	400	
	G	5,280,477	01/18/94	Trapp	370	85.1	
	H	5,289,580	02/22/94	Latif et al.	395	275	
	I	5,303,344	04/12/94	Yokoyama et al.	395	275	
	J	5,412,782	05/02/95	Hausman et al.	395	250	
	K	5,448,566	09/05/95	Richter et al.	370	94.1	

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	L	WO 00/13091	03/09/00	PCT/US98/24943				
	M	WO 99/65219	12/16/99	PCT/US99/13184				

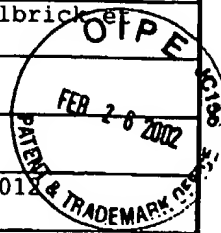
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	N	Internet pages entitled "Hardware Assisted Protocol Processing", (which Eugene Feinber is working on), 1 page, printed 11/25/98.
	O	Zilog product Brief entitled "Z85C30 CMOS SCC Serial Communication Controller", Zilog Inc., 3 pages, 1997.
	P	Internet pages of Xpoint Technologies www.xpoint.com website, 5 pages, printed 12/19/97.
	Q	Internet pages entitled: Asante and 100BASE-T Fast Ethernet. 7 pages, printed 5/27/97.

Examiner <i>[Signature]</i>	Date Considered <i>8/19/04</i>
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
<i>[Signature]</i>	A	5,485,579	01/16/96	Hitz et al.	395	200.12	
<i>[Signature]</i>	B	5,506,966	04/09/96	Ban	395	250	
<i>[Signature]</i>	C	5,511,169	04/23/96	Suda	395	280	
<i>[Signature]</i>	D	5,548,730	08/20/96	Young et al.	395	280	
<i>[Signature]</i>	E	5,558,121	12/24/96	Reddin et al.	395	200.15	
<i>[Signature]</i>	F	5,566,170	10/15/96	Bakke et al.	370	60	
<i>[Signature]</i>	G	5,590,328	12/31/96	Seno et al.	395	675	
<i>[Signature]</i>	H	5,592,622	01/07/97	Isfeld et al.	395	200.02	
<i>[Signature]</i>	I	5,629,933	05/13/97	Delp et al.	370	41	
<i>[Signature]</i>	J	5,634,099	05/27/97	Andrews et al.	395	200.07	
<i>[Signature]</i>	K	5,634,127	05/27/97	Cloud et al.	395	680	

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Foreign Patent Documents							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
<i>[Signature]</i>	L	WO 01/05107 A1	01/18/01	PCT/US00/19006			
<i>[Signature]</i>	M	WO 01/05116 A2	01/18/01	PCT/US00/19243			

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)	
<i>[Signature]</i>	N Internet pages entitled: A Guide to the Paragon XP/S-A7 Supercomputer at Indiana University. 13 pages, printed 12/21/98.
<i>[Signature]</i>	O Richard Stevens, "TCP/IP Illustrated, Volume 1, The Protocols", pages 325-326 (1994).
<i>[Signature]</i>	P Internet pages entitled: Northridge/Southbridge vs. Intel Hub Architecture, 4 pages, printed 2/19/01.
<i>[Signature]</i>	Q Gigabit Ethernet Technical Brief, Achieving End-to-End Performance. Alteon Networks, Inc., First Edition, September 1996.

Examiner <i>[Signature]</i>	Date Considered 8/19/04
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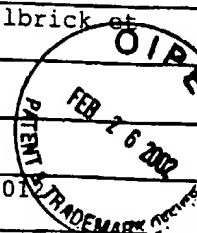
Inventors: Clive M. Philbrick et al.

Group Art Unit: 2182

INTELLIGENT NETWORK STORAGE INTERFACE SYSTEM

Examiner name: unknown

Attorney Docket No. ALA-01



U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	A	5,642,482	06/24/97	Pardillos	395	200.2	
	B	5,664,114	09/02/97	Krech, Jr. et al.	395	200.64	
	C	5,671,355	09/23/97	Collins	395	200.2	
	D	5,678,060	10/14/97	Yokoyama et al.	709	212	
	E	5,692,130	11/25/97	Shobu et al.	395	200.12	
	F	5,699,317	12/16/97	Sartore et al.	395	230.06	
	G	5,701,434	12/23/97	Nakagawa	395	484	
	H	5,701,516	12/23/97	Cheng et al.	395	842	
	I	5,749,095	05/05/98	Hagersten	711	141	
	J	5,751,715	05/12/98	Chan et al.	370	455	
	K	5,752,078	05/12/98	Delp et al.	395	827	

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Foreign Patent Documents

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	L	WO 01/05123 A1	01/18/01	PCT/US00/18976				
	M	WO 01/40960A1	06/07/01	PCT/US00/32660				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

N	Internet pages directed to Technical Brief on Alteon Ethernet Gigabit NIC technology, www.alteon.com, 14 pages, printed 3/15/97.
O	VIA Technologies, Inc. article entitled "VT8501 Apollo MVP4", pages i-iv, 1-11, cover and copyright page, revision 1.3, Feb. 1, 2000.
P	iReady News Archives article entitled "iReady Rounding Out Management Team with Two Key Executives", http://www.ireadyco.com/archives/keyexec.html, 2 pages, printed 11/28/98.
Q	"Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based On iReady's Design," Press Release October, 1998, 3 pages, printed 11/28/98.

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	A	5,758,084	05/26/98	Silverstein et al.	395	200.58	
	B	5,758,089	05/26/98	Gentry et al.	395	200.64	
	C	5,758,186	05/26/98	Hamilton et al.	395	831	
	D	5,758,194	05/26/98	Kuzma	395	886	
	E	5,771,349	06/23/98	Picazo, Jr. et al.	395	188.01	
	F	5,790,804	08/04/98	Osborne	395	200.58	
	G	5,794,061	08/11/98	Hansen et al.	395	800.01	
	H	5,802,580	09/01/98	McAlpice	711	149	
	I	5,809,328	09/15/98	Nogales et al.	395	200.43	
	J	5,812,775	09/22/98	Van Seeters et al.	395	200.43	
	K	5,815,646	09/29/98	Purcell et al.	395	163	

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							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	L	WO 01/04770 A2	01/18/01	PCT/US00/18939				
	M	WO/98/19412	05/07/98	PCT/US97/17257				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	N	Internet pages from iReady Products, web site http://www.ireadyco.com/products.html , 2 pages, downloaded 11/25/98.						
	O	iReady News Archives, Toshiba, iReady shipping Internet chip, 1 page, printed 11/25/98.						
	P	Interprophet article entitled "Technology", http://www.interprophet.com/technology.html , 17 pages, printed 3/1/00.						
	Q	iReady Corporation, article entitled "The I-1000 Internet Tuner", 2 pages, date unknown.						

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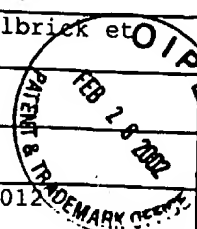
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	A	5,878,225	03/02/99	Bilansky et al.	395	200.57	
	B	5,913,028	06/15/99	Wang et al.	395	200.33	
	C	5,930,830	07/27/99	Mendelson et al.	711	171	
	D	5,931,918	08/03/99	Row et al	709	300	
	E	5,935,205	08/10/99	Murayama et al.	709	216	
	F	5,937,169	08/10/99	Connery et al.	395	200.33	
	G	5,941,969	08/24/99	Ram et al.	710	128	
	H	5,941,972	08/24/99	Hoese et al.	710	129	
	I	5,950,203	09/07/99	Stakuis et al.	707	128	
	J	5,991,299	11/23/99	Radogna et al.	370	392	
	K	6,005,849	12/21/99	Roach et al.	370	276	

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Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	L	WO/98/50852	11/12/98	PCT/US98/08719				
	M	WO/99/04343	01/28/99	PCT/US98/14729				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

N	iReady article entitled "About Us Introduction", Internet pages from http://www.iReadyco.com/about.html , 3 pages, printed 11/25/98.
O	iReady News Archive article entitled "Revolutionary Approach to Consumer Electronics Internet Connectivity Funded", San Jose, CA, November 20, 1997. 2 pages, printed 11/2/98.
P	iReady News Archive article entitled "Seiko Instruments Inc. (SII) INTRODUCES WORLD'S FIRST INTERNET-READY INTELLIGENT LCD MODULES BASED ON IREADY TECHNOLOGY," Santa Clara, CA and Chiba, Japan, October 26, 1998. 2 pages, printed 11/2/98.
Q	NEWSwatch article entitled "iReady internet Tuner to Web Enable Devices", Tuesday, November 5, 1996, printed 11/2/98.

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Inventors: Clive M. Philbrick
al.

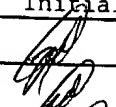
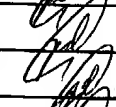
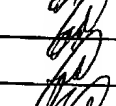
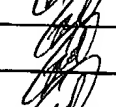
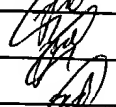
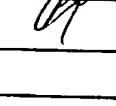
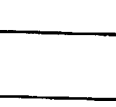
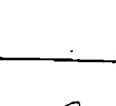



Group Art Unit: 2182

INTELLIGENT NETWORK STORAGE INTERFACE SYSTEM

Examiner name: unknown

Attorney Docket No. ALA-01

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	A	6,009,478	12/28/99	Panner et al.	710	5	
	B	6,016,513	01/18/00	Lowe	709	250	
	C	6,026,452	02/15/00	Pitts	710	56	
	D	6,034,963	03/07/00	Minami et al.	370	401	
	E	6,044,438	03/28/00	Olnowich	711	130	
	F	6,047,356	04/04/00	Anderson et al.	711	129	
	G	6,057,863	05/02/00	Olarig	345	520	
	H	6,061,368	05/09/00	Hitzelberger	370	537	
	I	6,065,096	05/16/00	Day et al.	711	114	
	J	6,141,705	10/31/00	Anand et al.	710	15	
	K	6,246,683	6/12/01	Connery et al.	370	392	

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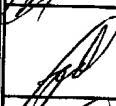

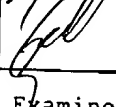
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Foreign Patent Documents

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							Yes	No
	L							
	M							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	N	EETimes article entitled "Tuner for Toshiba, Toshiba Taps iReady for Internet Tuner", by David Lammers, 2 pages, printed 11/02/98.
	O	"Comparison of Novell Netware and TCP/IP Protocol Architectures", by J.S. Carbone, 19 pages, printed 4/10/98.
	P	Adaptec article entitled "AEA-7110C-a DuraSAN product", 11 pages, printed 10/1/01.
	Q	iSCSI HBA article entitled "iSCSI and 2Gigabit fibre Channel Host Bus Adapters from Emulex, QLogic, Adaptec, JNI", 8 pages, printed 10/01/01.

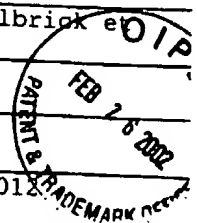
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<i>CP</i>	A	60/053,240		Jolitz et al.			07/18/97
	B						
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	M							

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<i>CP</i>	N	iSCSI HBA article entitled "FCE-3210/6410 32 and 64-bit PCI-to-Fibre Channel HBA", 6 pages, printed 10/01/01.
<i>CP</i>	O	iSCSI HBA article entitled "iSCSI Storage", 6 pages, printed 10/01/01.
<i>CP</i>	P	"Two-Way TCP Traffic Over Rate Controlled Channels: Effects and Analysis", by Kalampoukas et al., IEEE Transactions on Networking, vol. 6, no. 6, December 1998.
<i>CP</i>	Q	iReady News article entitled "Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based on iReady Design", Santa Clara, CA, and Tokyo, Japan, October 14, 1998, printed 11/2/98.

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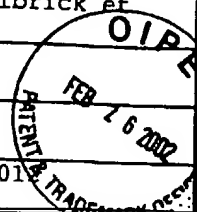
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L							
M							

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N	United States Patent Application No. 08/964,304, by Napolitano, et al., entitled "File Array Storage Architecture", filed 11/04/97.
O	"File System Design For An NFS File Server Appliance", Article by D. Hitz, et al., 13 pages.
P	Adaptec Press Release article entitled "Adaptec Announces EtherStorage Technology", 2 pages, May 4, 2000, printed 6/14/00.
Q	Adaptec article entitled "EtherStorage Frequently Asked Questions", 5 pages, printed 7/19/00.

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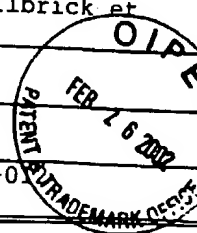
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	L							
	M							

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Adaptec article entitled "EtherStorage White Paper", 7 pages, printed 7/19/00.

CIBC World Markets article entitled "Computers; Storage", by J. Berlino et al., 9 pages, dated August 7, 2000.

Merrill Lynch article entitled "Storage Futures", by S. Milunovich, 22 pages, dated May 10, 2000.

CBS Market Watch article entitled "Montreal Start-Up Battles Data Storage Bottleneck", by S. Taylor, dated March 5, 2000, 2 pages, printed 3/7/00.

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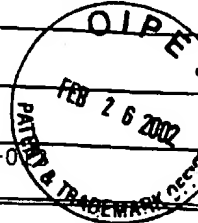
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Attorney Docket No. ALA-0



U.S. Patent Documents

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
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Foreign Patent Documents

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
L							
M							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

N	Internet-draft article entitled "SCSI/TCP (SCSI over TCP)", by J. Satran et al., 38 pages, dated February 2000, printed 5/19/00.
O	Internet pages entitled "Technical White Paper-Xpoint's Disk to LAN Acceleration Solution for Windows NT Server, printed 6/5/97.
P	Jato Technologies article entitled "Network Accelerator Chip Architecture, twelve-slide presentation, printed 8/19/98.
Q	EETimes article entitled "Enterprise System Uses Flexible Spec, dated August 10, 1998, printed 11/25/98.

Examiner

Date Considered

8/19/04

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

U.S. Department of Commerce, Patent and Trademark Office

Application No.: 09/802,551

Filing date: March 9, 2001

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

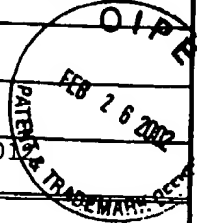
Inventors: Clive M. Philbrick et al.

Group Art Unit: 2182

INTELLIGENT NETWORK STORAGE INTERFACE SYSTEM

Examiner name: unknown

Attorney Docket No. ALA-01



U.S. Patent Documents

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
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Foreign Patent Documents

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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

N	Internet pages entitled "Smart Ethernet Network Interface Cards", which Berend Ozceri is developing, printed 11/25/98.
O	Internet pages entitled "Hardware Assisted Protocol Processing", which Eugene Feinberg is working on, printed 11/25/98.
P	Internet pages of Xaqti corporation entitled "GigaPower Protocol Processor Product Review," printed 11/25/99.
Q	Internet pages entitled "DART: Fast Application Level Networking via Data-Copy Avoidance," by Robert J. Walsh, printed 6/3/99.

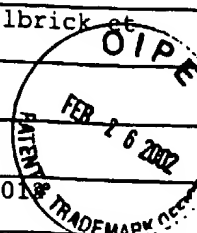
Examiner

Date Considered

8/19/04

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U.S. Department of Commerce, Patent and Trademark Office						Application No.: 09/802,551			
						Filing date: March 9, 2001			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT						Inventors: Clive M. Philbrick et al.			
						Group Art Unit: 2182			
INTELLIGENT NETWORK STORAGE INTERFACE SYSTEM						Examiner name: unknown.			
						Attorney Docket No. ALA-018			



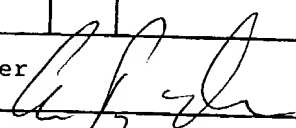
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Foreign Patent Documents							
		Document Number	Date	Country	Class	Subclass	Translation
	L						Yes No
	M						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)	
N	Internet pages of InterProphet entitled "Frequently Asked Questions", by Lynne Jolitz, printed 6/14/00.
O	Internet pages entitled "iReady About Us and iReady Products, printed 11/25/98.
P	Andrew S. Tanenbaum, Computer Networks, Third Edition, 1996, ISBN 0-13-349945-6.
Q	

Examiner 	Date Considered 8/19/04
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U.S. Department of Commerce, Patent and Trademark Office		Applicant No.: 09/802,551
		Filing date: March 9, 2001
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Inventors: Clive M. Philbrick et al.
FEB 26 2002		Group Art Unit: 2182
INTELLIGENT NETWORK STORAGE INTERFACE SYSTEM		Examiner name: unknown
		Attorney Docket No. ALA-012

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U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	AA	5,996,024	11/30/99	Blumenau	709	301	
	AB	6,226,680	05/01/01	Boucher et al.	709	230	
	AC	6,247,060	06/12/01	Boucher et al.	709	238	
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Foreign Patent Documents

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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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Examiner	Date Considered 8/19/04
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

U.S. Department of Commerce, Patent and Trademark Office		Application No.: 09/802,551
SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Filing date: March 9, 2001
INTELLIGENT NETWORK STORAGE INTERFACE SYSTEM		Inventors: Clive M. Philbrick et al.
MAR 29 2002		Group Art Unit: 2182
		Examiner name: Unknown
		Attorney Docket No. ALA-012

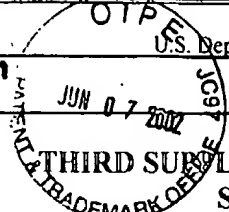
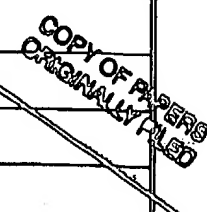
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	AA	6,173,333	07/18/97	Jolitz et al.			
	AB	2001/0025315A1		Jolitz			01/10/01
	AC	2001/0004354A1		Jolitz			01/10/01
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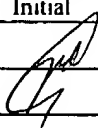
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Foreign Patent Documents								
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	AN							
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)	
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Examiner	Date Considered <u>8/19/04</u>

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

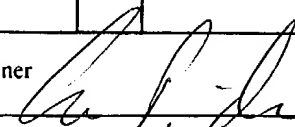
					U.S. Department of Commerce, Patent and Trademark Office			Application No.: 09/802,551		
THIRD SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Filing date: March 9, 2001					
					Inventors: Clive M. Philbrick et al.					
INTELLIGENT NETWORK STORAGE INTERFACE SYSTEM					Group Art Unit: 2182					
					Examiner name: Unknown					
					Attorney Docket No. ALA-012					

U.S. Patent Documents							
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	A	6,345,301	02/05/02	Burns et al.	709	230	
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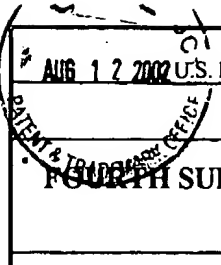
Foreign Patent Documents								
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)	
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Examiner 	Date Considered <u>8/19/04</u>	
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*EXAMINER: Initial reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

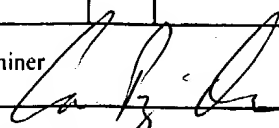
					Application No.: 09/802,551		
FOURTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Filing date: March 9, 2001		
					Inventors: Clive M. Philbrick et al.		
					Group Art Unit: 2182		
INTELLIGENT NETWORK STORAGE INTERFACE SYSTEM					Examiner name: Unknown		
					Attorney Docket No. ALA-012		

U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
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Foreign Patent Documents							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	L							
	M							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
	N	Form 10-K for Exelan, Inc., for the fiscal year ending December 31, 1987 (10 pages).
	O	Form 10-K for Exelan, Inc., for the fiscal year ending December 31, 1988 (10 pages).
	P	
	Q	

Examiner 	Date Considered 8/19/04
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

U.S. Department of Commerce, Patent and Trademark Office

Application No.: 09/802,551

FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Filing date: March 9, 2001

Inventors: Clive M. Philbrick et al.

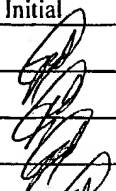
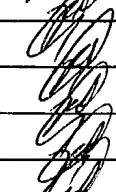
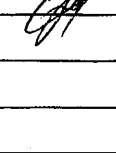
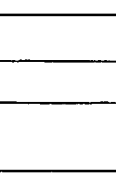
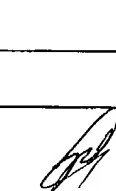
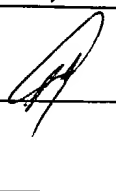
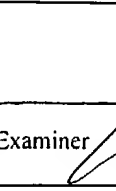
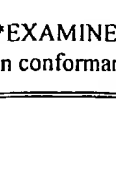
Group Art Unit: 2182

Examiner name: Unknown

Attorney Docket No. ALA-012


 INTERLIGENT NETWORK STORAGE INTERFACE SYSTEM

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	A	5,058,110	10/15/91	Beach et al.	370	85.6	
	B	6,021,446	02/01/00	Gentry, Jr.	709	303	
	C	6,356,951	03/12/02	Gentry, Jr.	709	250	
	D	6,389,468	05/14/02	Muller et al.	709	226	
	E	6,427,169	07/30/02	Elzur	709	224	
	F	6,434,651	08/13/02	Gentry, Jr.	710	260	
	G	6,449,656	09/10/02	Elzur et al.	709	236	
	H	6,453,360	09/17/02	Muller et al.	709	250	
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Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	L							
	M							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	N	Article from Rice University entitled "LRP: A New Network Subsystem Architecture for Server Systems", by Peter Druschel and Gaurav Banga, Rice University, October 1996, 15 pages.
	O	Internet RFC/STD/FYI/BCP Archives article with heading "RFC2140" entitled "TCP Control Block Interdependence", web address http://www.faqs.org/rfcs/rfc2140.html , 9 pages, printed 9/20/02.
	P	
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Examiner

Date Considered

8/19/04

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U.S. Department of Commerce, Patent and Trademark Office

Application No.: 09/802,551

**6th SUPPLEMENTAL INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

Filing date: March 9, 2001

Inventors: Clive M. Philbrick et al.

Group Art Unit: 2182

Examiner name: Unknown

Attorney Docket No. ALA-012

INTELLIGENT NETWORK STORAGE INTERFACE SYSTEM

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
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Foreign Patent Documents

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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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Examiner

Date Considered

8/19/04

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U.S. Department of Commerce, Patent and Trademark Office

Application No.: 09/802,551

**SUPPLEMENTAL INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

Filing date: March 9, 2001

Inventors: Clive M. Philbrick et al.

Group Art Unit: 2182

Examiner name: Unknown

Attorney Docket No. ALA-012

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	A	5,517,668	5/14/1996	Szwerinski et al.	395	800	
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Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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Date Considered

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